## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant: Joseph J. Karniewicz | Examiner: Thai Phan |
| Serial No.: 09/031,326 | Group Art Unit: 2123 |
| Filed: February 26, 1998 | Docket: 303.376US1 |
| Title: PARAMETER POPULATION OF CELLS OF A HIERARCHICAL | |
| SEMICONDUCTOR STRUCTURE VIA FILE RELATION | |

### RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
Washington, D.C. 20231

### REMARKS

Applicant has reviewed and considered the Office Action mailed on September 7, 2001,
and the Robinson et al patent cited therewith.

Claims 2 through 25 remain pending in this application.

### *Information Disclosure Statement*

Applicant respectfully requests that a copy of the 1449 Form, listing all references that
were submitted with the Supplemental Information Disclosure Statement filed on January 9,
2001, marked as being considered and initialed by the Examiner, be returned with the next
official communication.

### *§102 Rejection of the Claims*

Claims 1-25 were rejected under 35 USC § 102(b) as being anticipated by Robinson et al.
(U.S. Patent No. 5,524,244).

The newly cited Robinson et al patent relates to a system for dividing a processing task
into tasks for a programmable real-time signal processor (SPROC) and tasks for a decision
making microprocessor. The system is described as being programmed in a manner requiring
entry of nothing more than a block diagram of a user's design. The block diagram is then
"implemented into silicon". The patent discusses a SPROC cells Function Library that "contains
over fifty predefined functions which can be used through the graphical interface of the
SPROClab development system" (col 36, lines 43 - 45). While the Robinson et al patent may
show that the SPROCcells are "design cells" useful for putting multiple instances of themselves